

L Number	Hits	Search Text	DB	Time stamp
1	145	712/37.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 15:36
2	1269	711/103.ccls. or 711/211.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 15:37
3	2252	365/185.33.ccls. or 365/189.02.ccls. or 365/230.02.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 15:41
4	2881	326/37-41.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 15:42
5	545	(pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:07
6	89	326/37-41.ccls. and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:08
7	68	(326/37-41.ccls. and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:28
8	2840	(pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:40
9	33	((326/37-41.ccls. and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:08
10	1073	(pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector or switch\$3) same (prom or eeprom or flash or eprom or rom)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:07
11	545	((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector or switch\$3) same (prom or eeprom or flash or eprom or rom)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:08
12	443	((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector or switch\$3) same (prom or eeprom or flash or eprom or rom)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:08

13	83	(((((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector or switch\$3) same (prom or eeprom or flash or eprom or rom)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:26
14	50	(((((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector or switch\$3) same (prom or eeprom or flash or eprom or rom)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration)) not (((326/37-41.ccls. and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:27
15	1313	713/100.ccls. or 710/10.ccls. or 710/51.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:24
16	4784	712/37.ccls. or (711/103.ccls. or 711/211.ccls.) or (365/185.33.ccls. or 365/189.02.ccls. or 365/230.02.ccls.) or (713/100.ccls. or 710/10.ccls. or 710/51.ccls.)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:24
17	33	(((((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector or switch\$3) same (prom or eeprom or flash or eprom or rom)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration) and 326/37-41.ccls.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:26

18	0	(((((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector or switch\$3) same (prom or eeprom or flash or eprom or rom)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration) and 326/37-41.ccls.) not (((326/37-41.ccls. and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:27
19	21	(712/37.ccls. or (711/103.ccls. or 711/211.ccls.) or (365/185.33.ccls. or 365/189.02.ccls. or 365/230.02.ccls.) or (713/100.ccls. or 710/10.ccls. or 710/51.ccls.)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector or switch\$3) same (prom or eeprom or flash or eprom or rom))	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:27
21	17	((712/37.ccls. or (711/103.ccls. or 711/211.ccls.) or (365/185.33.ccls. or 365/189.02.ccls. or 365/230.02.ccls.) or (713/100.ccls. or 710/10.ccls. or 710/51.ccls.)) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration) and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) same (mux or multiplex\$3 or selector or switch\$3) same (prom or eeprom or flash or eprom or rom))) and (@ad<=19990811 or @rlad<=19990811)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:38
22	10	baier-heinz\$.in.	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:40
23	1	baier-heinz\$.in. and ((pga or fpga or dfpga or dpga or fpla or pla or (programmable adj4 array)) with configuration)	USPAT; US-PGPUB; EPO; JPO; DERWENT; IBM_TDB	2004/04/25 16:40